PATENT ABSTRACTS OF JAPAN

(11) Publication number : 09-246996

(43)Date of publication of application : 19.09.1997

G11B 20/18 G11B 20/18 H04N 5/92 HO3M 13/00 GO6F 11/10 20/18 G11B (51) Int. CI

(21) Application number : 08-079502 (71) Applicant : (22) Date of filing : 07.03.1996 (72) Inventor :

icant : SONY CORP

ABE TAKAO KOIKE HIDEYUKI YAMASAKI KENJI KAWAHARA MINORU

(54) ERROR CORRECTION DEVICE

(57) Abstract:

PROBLEM TO BE SOLVED: To eliminate useless power consumption in an error correction device for decoding data error correction encoded by a product code.

supplied to an internal code decoder 23 and error corrected and the data are written to a RAM 30 through a interface between them is eliminated, and in the case that the error correction by an external code is not At the time, by the stoppage of clock supply or the like for instance, the operation of the decoder 31 is external code decoder 31 is not required, the data are read from the RAM 30 and outputted to the outside. controller 29. In the case that all errors are corrected in the decoder 23, since error correction by an required, the operation of the decoder 31 is stopped. Thus, the useless power consumption is suppressed. SOLUTION: The data supplied to an error correction decoder 9 constituted as one integrated circuit are stopped. Since the decoders 23 and 31 are constituted as the same integrated circuit, the need of an

